



N-Channel Enhancement Mode MOSFET

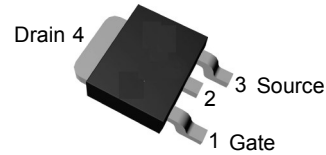
Features

- 20V/50A,
 $R_{DS(ON)} = 12m\Omega$ (Max.) @ $V_{GS} = 4.5V$
 $R_{DS(ON)} = 14.5m\Omega$ (Max.) @ $V_{GS} = 2.5V$
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

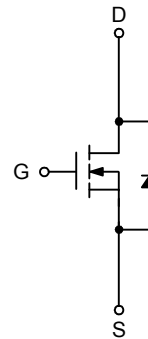
Applications

- Power Motor Controls.
- High Frequency Isolated DC-DC Converters with Synchronous Rectification for Industrial.
- Load Switching.

Pin Description



Top View of TO-252-2



N-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		20	V
V _{GSS}	Gate-Source Voltage		±12	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _C =25°C	20	A
I _D	Continuous Drain Current	T _C =25°C	50*	A
		T _C =100°C	70	
I _{DM} ^a	Pulse Drain Current Tested	T _C =25°C	260	A
P _D	Maximum Power Dissipation	T _C =25°C	50	W
		T _C =100°C	20	
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	2.5	°C/W
I _D	Continuous Drain Current	T _A =25°C	18	A
		T _A =70°C	15	
P _D	Maximum Power Dissipation	T _A =25°C	2.5	W
		T _A =70°C	1.6	
R _{θJA}	Thermal Resistance-Junction to Ambient	t ≤ 10s	20	°C/W
		Steady State	50	
I _{AS} ^b	Avalanche Current, Single pulse	L=0.5mH	19	A
E _{AS} ^b	Avalanche Energy, Single pulse	L=0.5mH	212	mJ

Note a : *Current is limited by bond wire.

Note b : UIS tested and pulse width are limited by maximum junction temperature 150°C (initial temperature $T_J=25^{\circ}\text{C}$).

Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

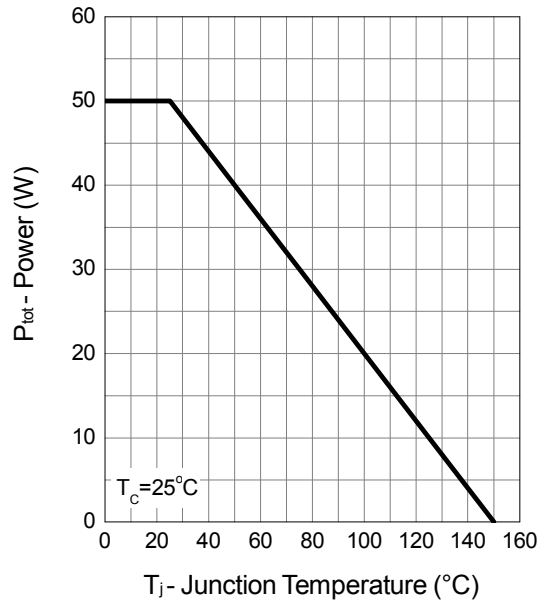
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V T _J =85°C	-	-	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	0.5	0.75	1	V
I _{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^c	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =50A	-	7.2	8	mΩ
		T _J =125°C	-	6.9	-	
		V _{GS} =4.5V, I _{DS} =50A	-	9.0	12	
		V _{GS} =2.5V, I _{DS} =40A	-	11	14.5	
Diode Characteristics						
V _{SD} ^c	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.7	1.1	V
t _{rr}	Reverse Recovery Time	I _{DS} =20A, dI _{SD} /dt=100A/μs	-	16	-	ns
t _a	Charge Time		-	9.2	-	
t _b	Discharge Time		-	7	-	
Q _{rr}	Reverse Recovery Charge		-	7.5	-	nC
Dynamic Characteristics ^d						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.5	2.7	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =10V, Frequency=1.0MHz	-	780	1015	pF
C _{oss}	Output Capacitance		-	170	-	
C _{rss}	Reverse Transfer Capacitance		-	120	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =10V, R _L =10Ω, I _{DS} =1A, V _{GEN} =4.5V, R _G =1Ω	-	10.6	-	ns
t _r	Turn-on Rise Time		-	15	-	
t _{d(OFF)}	Turn-off Delay Time		-	17.2	-	
t _f	Turn-off Fall Time		-	4	-	
Gate Charge Characteristics ^d						
Q _g	Total Gate Charge	V _{DS} =10V, V _{GS} =4.5V, I _{DS} =50A	-	8.9	11.5	nC
Q _g	Total Gate Charge	V _{DS} =10V, V _{GS} =4.5V, I _{DS} =50A	-	50	-	
Q _{gth}	Threshold Gate Charge		-	1.25	-	
Q _{gs}	Gate-Source Charge		-	0.52	-	
Q _{gd}	Gate-Drain Charge		-	3.8	-	

Note c : Pulse test ; pulse width≤300μs, duty cycle≤2%.

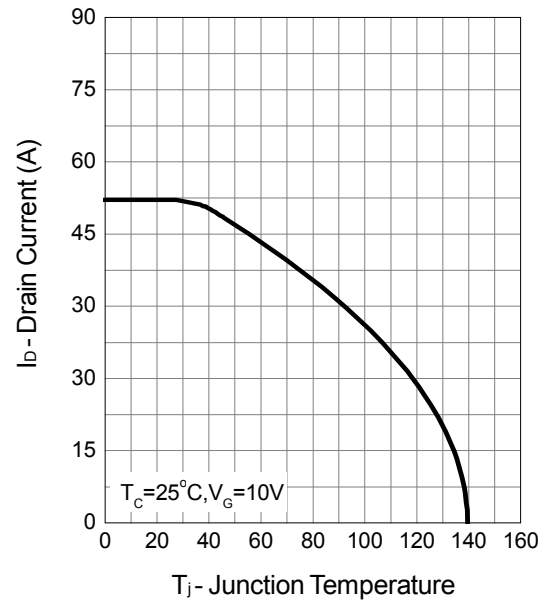
Note d : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

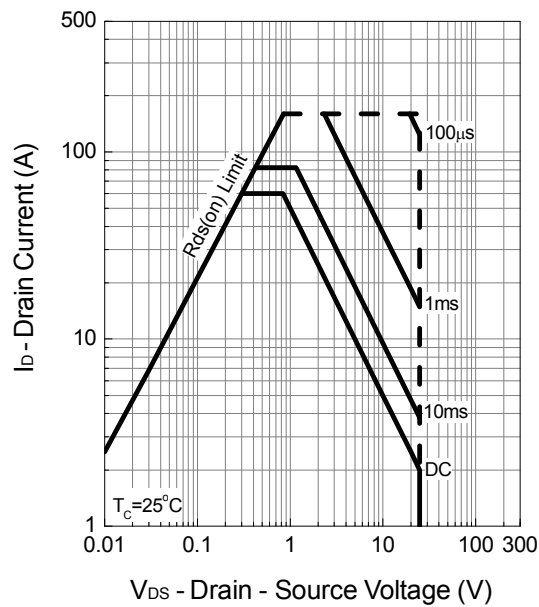
Power Dissipation



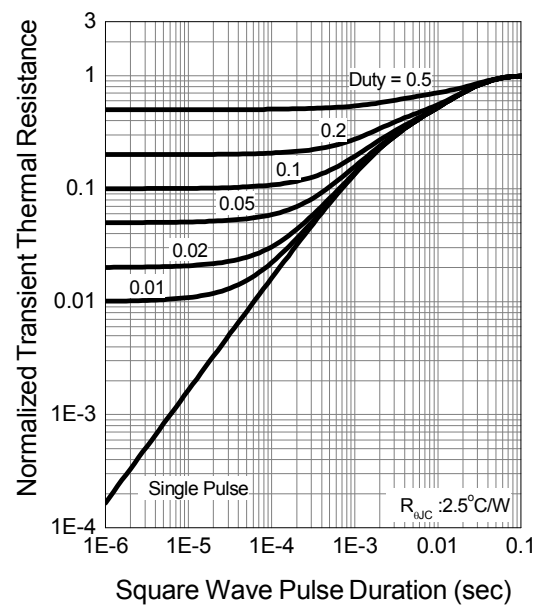
Drain Current



Safe Operation Area



Thermal Transient Impedance



Typical Operating Characteristics (Cont.)

